FIG. 1

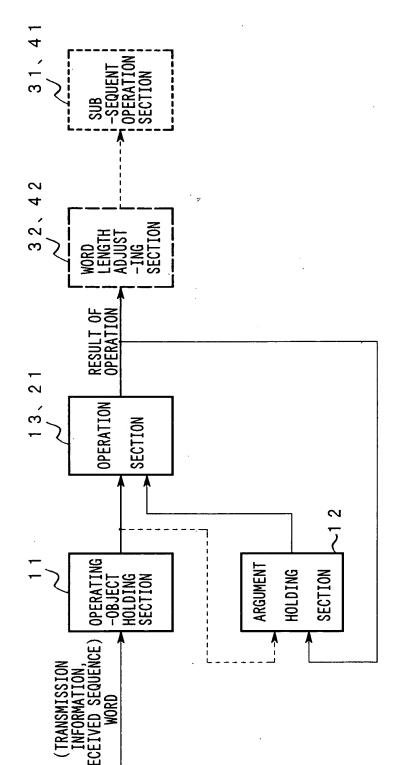
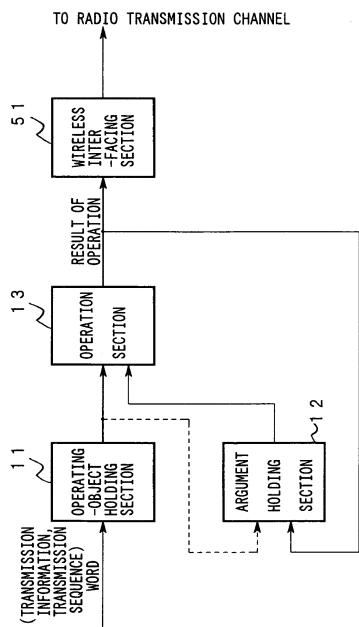
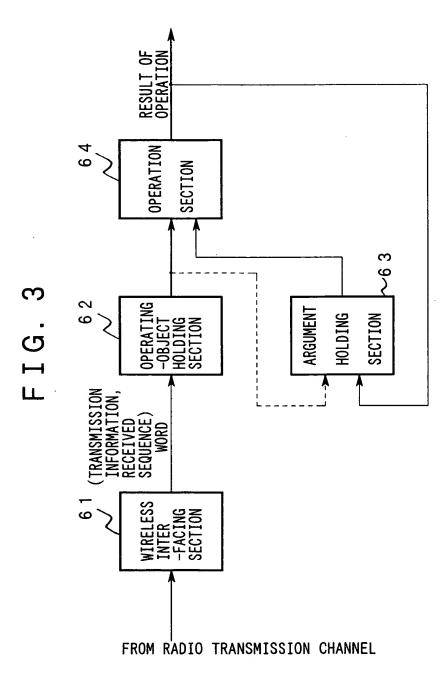


FIG. 2

that you have not the first that the



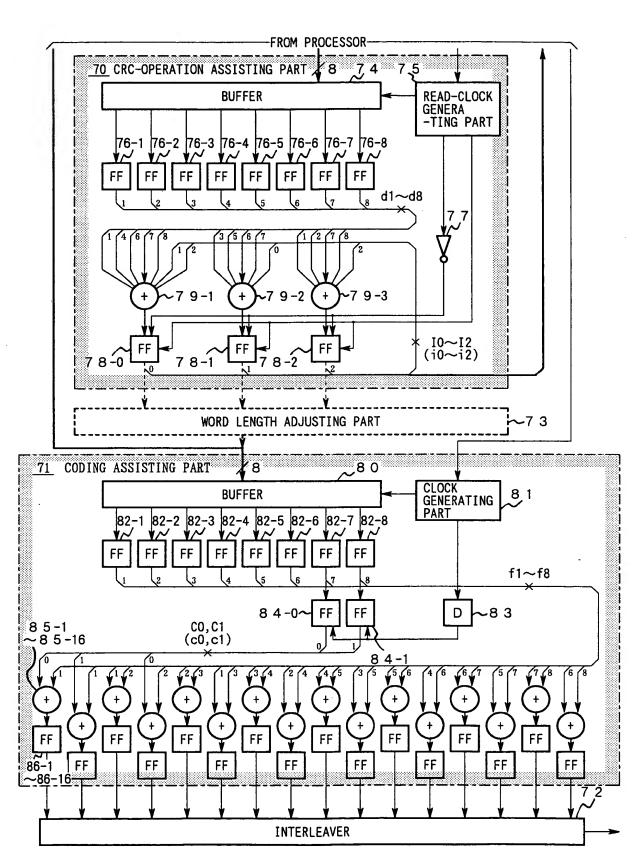


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FIG. 4



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## FIG. 5

	LOGICAL VALUE OBTAINED	LOGICAL VALUR HELD BY	LOGICAL VALUR HELD BY	LOGICAL VALUE OBTAINED LOGICAL VALUR HELD BY LOGICAL VALUR HELD BY LOGICAL VALUE OBTAINED AT THE OUTPUT	LOGICAL VALUR HELD BY THE
	AT THE OUTPUT OF THE EXCLUSIVE-OR GATE 129	THE FLIP-FLOP 130-1	THE FLIP-FLOP 130-2	OF THE EXCLUSIVE-OR GATE 131	FLIP-FLOP 130-3
1		0!	11		i2
Ġ.	i2⊕d1	i2⊕d1	10	i2⊕d1⊕i1	i1⊕i2⊕d1
d2	i1⊕i2⊕d1⊕d2	11⊕12⊕d1⊕d2	12@d1	i1⊕i2⊕d1⊕d2⊕i0	100110120d10d2
<del>g</del> 3	10⊕11⊕12⊕d1⊕d2⊕d3	00011001200010000000000000000000000000	11⊕i2⊕d1⊕d2	10@i1@i2@d1@d2@d3@i2@d1	i0⊕i1⊕d2⊕d3
44	i0⊕i1⊕d2⊕d3⊕d4	10⊕11⊕d2⊕d3⊕d4	10⊕11⊕12⊕d1⊕d2⊕d3	100110042000000000000000000000000000000	10⊕12⊕d1⊕d3⊕d4
d5	10@12@d1@d3@d4@d5   10@12@d1@d3@c	10⊕12⊕d1⊕d3⊕d4⊕d5	d4⊕d5   i0⊕i1⊕d2⊕d3⊕d4	100120010010010010011012	i1⊕d2⊕d4⊕d5
				<b>⊕</b> d1 <b>⊕</b> d2 <b>⊕</b> d3	
90	i1⊕i2⊕d4⊕d5⊕d6	i1⊕d2⊕d4⊕d5⊕d6	i0⊕i2⊕d1⊕d3⊕d4⊕d5	110020044000000000000000000000000000000	i0⊕d3⊕d5⊕d6
d7	i0⊕d3⊕d5⊕d6⊕d7	i0⊕d3⊕d5⊕d6⊕d7	11⊕d2⊕d4⊕d5⊕d6	$10\oplus d3\oplus d5\oplus d6\oplus d7\oplus 10\oplus 12\oplus d1\oplus d3\oplus d4$   $12\oplus d1\oplus d4\oplus d6\oplus d7$	i2⊕d1⊕d4⊕d6⊕d7
				⊕d5	
<b>8</b>		=01	11=		12=
	i2@d1@d4@d6@d7@d8	12⊕d1⊕d4⊕d6⊕d7⊕d8	d7@d8   10@d3@d5@d6@d7   12@d1@d4@d6@d7@d8	12⊕d1⊕d4⊕d6⊕d7⊕d8	i:1⊕i2⊕d1⊕d2⊕d5⊕d7⊕d8
				⊕i1⊕d2⊕d4⊕d5⊕d6	

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FIG. 6

	LOGICAL VALUE HELD	LOGICAL VALUE HELD	LOGICAL VALUE HELD	OGICAL VALUE HELD   LOGICAL VALUE HELD   LOGICAL VALUE HELD   LOGICAL VALUE OBTAINED   LOGICAL VALUE OBTAINED	LOGICAL VALUE OBTAINED
	BY THE FLIP-FLOP	BY THE FLIP-FLOP	BY THE FLIP-FLOP	AT THE OUTPUT OF THE	AT THE OUTPUT OF THE
	131-1	131-2	131-3	EXCLUSIVE-OR GATE	EXCLUSIVE-OR GATE
				132-1	132-2
	00	10	c2	1	-
fl	fl	00	c1	$g1 = c0 \oplus f1$	g2 =c1⊕f1
f2	f2	f1	00	$g3 = f1 \oplus f2$	g4 = c0⊕f2
f3	f3	f2	f1	g5 = f2⊕f3	g6 =f1⊕f3
f4	f4	f3	f2	g7 =f3⊕f4	g8 = f2⊕f4
fS	f5	f4	f3	g9 =f4⊕f5	g10=f3⊕f5
g.	f6	f5	f4	g11=f5⊕f6	g12=f4⊕f6
f7	f7	f6	f5	g13=f6⊕f7	g14=f5⊕f7
<u>æ</u>	£	£7	f6	$g15=f7\oplus f8$	g16=f6⊕f8

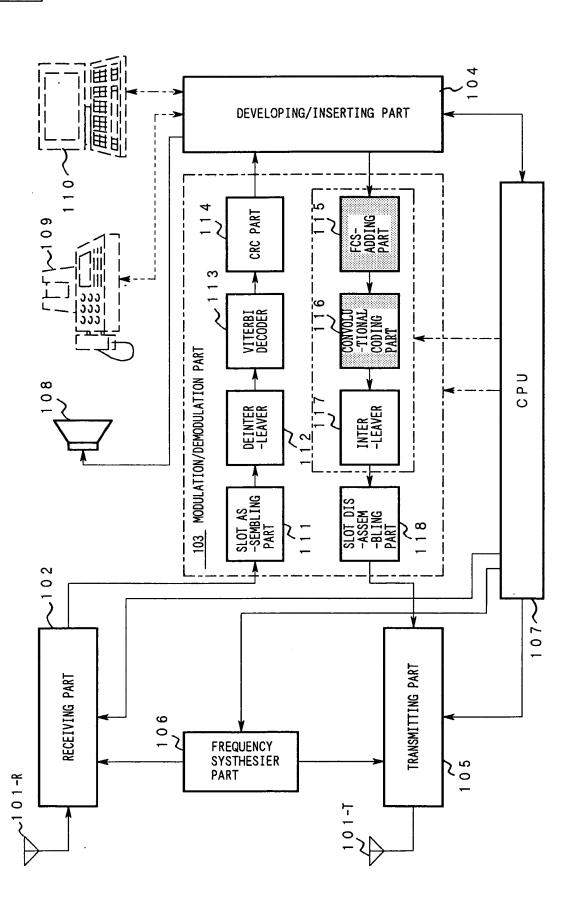
Hard Hard Control Cont

READ-CLOCK GENERATING PART 5 A~ EXEPTIONAL PROCESSING PART 9 2 -3 -FROM PROCESSOR-9 2-5 9 2-1 91 8-9/ / 出 1 9-91 6 0 Ή 7 8-2 TO WORD LENGTH ADJUSTING PART 76-4 76-5 BUFFER  $\infty$ H 78-1 90 CRC OPERATION ASSISTING PART 76-1 7 9-1 7 8 -0

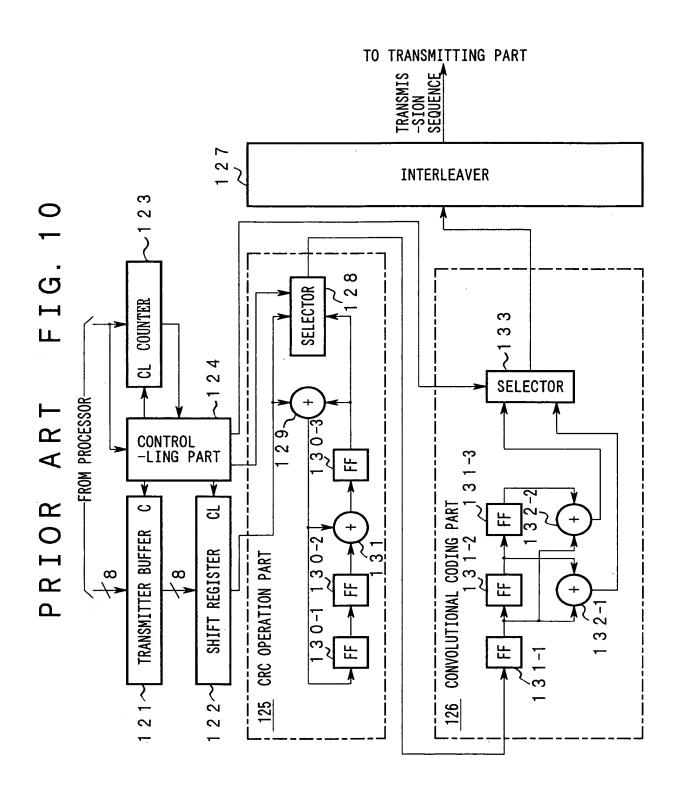
## FIG. 8

	MSB	LSB
FIRST BYTE	d 1 d 2	
	INVALID BITS (Logical value is not n	ecessarily "0"
SECOND BYTE	d 3 d 4 d 5 d 6 d 7 d 8	d 9 d 10
THIRD BYTE	d 11 d 12 d 13 d 14 d 15 d 16	d 17 d 18

FIG. 9

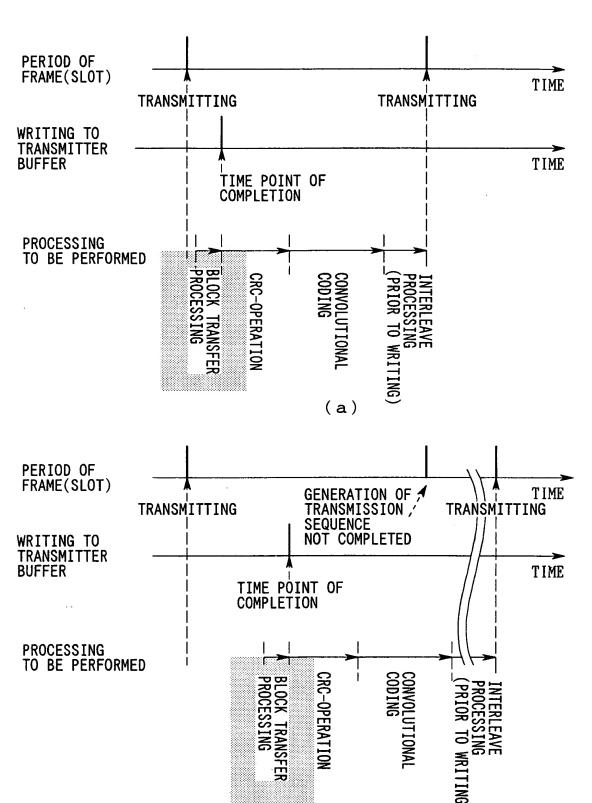


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## PRIOR ART FIG. 11



(b)